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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0

Title of Invention	High-Voltage Transistor With Multi-Layered Conduction Region																												
<p>Application Number: 09/961229 Confirmation Number: 4375 First Named Applicant: Vladimir Rumennik Attorney Docket Number: 03692.P007XD4 Art Unit: 2811 Examiner: Shouxiang Hu. Search string: (4614959 or 5294824).pn.</p> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>4614959</td><td>1986-09-01</td><td>Nakagawa</td><td></td><td></td><td></td></tr><tr><td></td><td>2</td><td>5294824</td><td>1994-03-01</td><td>Okada</td><td></td><td></td><td></td></tr></tbody></table> <p>Signature</p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td></td><td></td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	4614959	1986-09-01	Nakagawa					2	5294824	1994-03-01	Okada				Examiner Name	Date		
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2811

Electronic Filing System (EFS) Data
Electronic Patent Application Submission
USPTO Use Only

EFS ID: 41161
Application ID: 09961229
Title of Invention: High-Voltage Transistor With
Multi-Layered Conduction
Region
First Named Inventor: Vladimir Rumennik
Domestic/Foreign Application: Domestic Application
Filing Date: 2001-09-20
Effective Receipt Date: 2003-05-29
Submission Type: Information Disclosure
Statement
Filing Type:
Confirmation number: 4375
Attorney Docket Number: 03692.P007XD4



Total Fees Authorized:

Digital Certificate Holder: cn=Bradley John Berezna,ou=Registered Attorneys,ou=Patent and
Trademark Office,ou=Department of Commerce,o=U.S. Government,c=US
Certificate Message Digest: f776aa385e9246f7171526b5e5f8011b3cf98756

**TRANSMITTAL**

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	High-Voltage Transistor With Multi-Layered Conduction Region									
Application Number: 09/961229 Date: 2001-09-20 First Named Applicant: Vladimir Rumennik										
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<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Bradley J Berezna Registered Number: 33,474</td><td>BJB</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	Bradley J Berezna Registered Number: 33,474	BJB	Attorney		
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<table><tr><td>Documents being submitted</td><td>Files</td></tr><tr><td>us-ids</td><td>03692.P007XD4-usidst.xml</td></tr><tr><td></td><td>us-ids.dtd</td></tr><tr><td></td><td>us-ids.xsl</td></tr></table>			Documents being submitted	Files	us-ids	03692.P007XD4-usidst.xml		us-ids.dtd		us-ids.xsl
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us-ids	03692.P007XD4-usidst.xml									
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Comments										